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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al. Art Unit : 2815
Serial No. : 09/736,724 Examiner : Paul E. Brock
Filed : December 13, 2000
Title : INSULATED GATE SEMICONDUCTOR DEVICE AND METHOD OF
MANUFACTURING THE SAME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

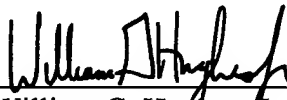
INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449.

This statement is being filed after a first Office action on the merits, but before receipt of a final Office action or a Notice of Allowance. A check for \$180 in payment of the late submission fee of §1.17(p) is enclosed. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

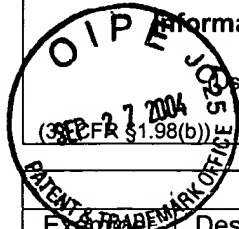
Date: September 27, 2004



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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 07977-175002	Application No. 09/736,724
Information Disclosure Statement by Applicant (Use several sheets if necessary)		Applicant Yamazaki, et al.	
		Filing Date December 13, 2000	Group Art Unit 2815

**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	4,757,032	07/12/1988	CONTIERO			09/06/1985
	AB	5,675,164	10/07/1997	BRUNNER et al.			06/07/1995
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	AD	5,905,291	05/18/1999	UTSUNOMIYA et al.			07/18/1995

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	AE	JP 53-068177	17 JUN 1978	JAPAN			Abstract	
	AF	JP 54-157481	12 DEC 1979	JAPAN			Abstract	
	AG	JP 55-151363	25 NOV 1980	JAPAN			Abstract	
	AH	JP 01-223741	06 SEP 1989	JAPAN			Abstract	
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	AO	JP 06-252392	09 SEP 1994	JAPAN			Abstract	
	AP	JP 07-131009	19 MAY 1995	JAPAN			Abstract	
	AQ							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AR	Ng. Kwok, "Complete Guide to Semiconductor Devices," January 1, 1995; pages 164-165.
	AS	
	AT	
	AU	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	